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| David Ljung Madison Stellar

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|   415-341-5555 |  hire@DaveSource.com |  Silicon Valley, CA |

CAREER SUMMARYCPU verification expert who can take on large verification tasks as well as toolchain creation, ready to fit in at your organization and make a difference. [*Resume v5.3, 2018/05*](http://Resume.DaveSource.com/)SKILLSSelf-starter capable of writing everything from tools to testbenches to test generators.Computer LanguagesMulti-linguistic: Ruby, Perl, C, Java, Verilog, SystemVerilog, assembly, etc.. I often become the ruby/perl resource where I work.ACCOMPLISHMENTSCreated entire custom verification toolchain for multiple companies.Invented an original formal verification technique which went far beyond any industry standard and found all bugs in tested blocks in pre-silicon, see: <http://DaveSource.com/Successes/Formal-Blocks/>Have taken ownership of verifying a complete CPU at multiple companies, using techniques such as creating modular testbenches Rewrote an entire post-silicon debug toolchain months ahead of schedule at a savings of $3 million per day according to corporate calculations.Specializes in creating state-of-the-art code generators such as a highly sophisticated reverse code packer for a custom VLIW CPU that [builds itself based off of parsing the specification](http://davesource.com/Successes/Fullchip-New-Architecture/).Accomplished at writing Directed-Random testbenches which provide over 95% of coverage.Great at novel approaches to [breaking things](http://davesource.com/Bugs/), which is why I've posted bugs in almost every tool I've used, including established tools such as compilers, linkers, shells, assemblers.Designed/implemented custom shipping operations algorithms as a test pilot for DHL, accomplishing an order of magnitude improvement in runtime **and** savings.Creator of shareware tools including '[album](http://marginalhacks.com/Hacks/album/),' the world's #1 CLI album generator.EMPLOYMENT / EDUCATION

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| Jan 2017 | - Present | [NVidia](http://NVidia.com/) | Senior GPU Verification Engineer |
| May 2015 | - Jan 2016 | [Bluechip Systems](http://BluechipSys.com/) | Verification Consultant |
| Feb 2007 | - Dec 2013 | iCelero, LLC (3Plus1 Tech) | Director of Verification |
| Jul 2002 | - Present | [DaveSource Consulting](http://DaveSource.com/) | Head Engineer |
| Jan 2000 | - Jul 2002 | [Transmeta Corp](http://Transmeta.com/) | CPU Verification Engineer |
| Jul 1998 | - Jan 2000 | SandCraft, Inc | CPU Verification Lead |
| Aug 1994 | - Jun 1998 | Hewlett-Packard | CPU Verifcation and Debug |

University of Wisconsin, Madison: Electrical Computer Engineering / Computer Science [GPA: 3.0] |
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